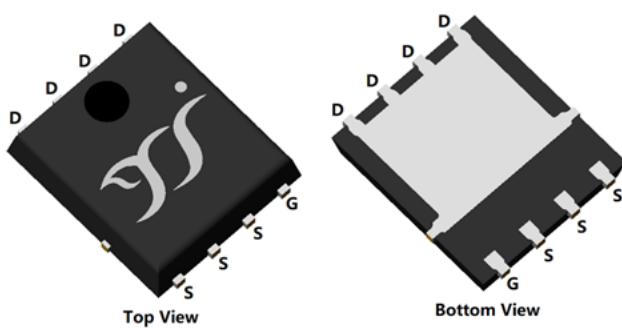
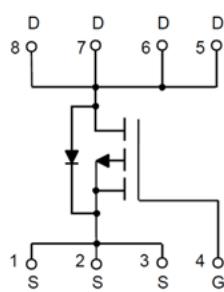


P-Channel Enhancement Mode Field Effect Transistor



PDFN5060-8L



Product Summary

- V_{DS} -150V
- I_D -14A
- $R_{DS(on)}$ (at $V_{GS}=-10V$) $<150m\Omega$
- $R_{DS(on)}$ (at $V_{GS}=-4.5V$) $<180m\Omega$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Split gate trench MOSFET technology
- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power management
- Portable equipment

■ Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			V_{DS}	-	-150	V
Gate-source Voltage			V_{GS}	-20	20	
Continuous Drain Current (Note 1,2)	Steady-State	$T_A=25^\circ C, V_{GS}=-10V$	I_D	-	-2.7	A
Continuous Drain Current (Note 1,3)		$T_A=100^\circ C, V_{GS}=-10V$		-	-1.7	
Pulsed Drain Current	$T_C=25^\circ C, t_p \leq 10\mu s$		I_{DM}	-	-50	
Maximum Body-Diode Continuous Current	$T_C=25^\circ C$		I_S		-14	
Avalanche Energy (non-repetitive)	$T_J=25^\circ C, V_G=-10V, R_G=25\Omega, L=0.5mH, I_{AS}=-19A$		EAS	-	90.25	mJ
Total Power Dissipation (Note 1,2)	Steady-State	$T_A=25^\circ C$	P_D	-	2.2	W
Total Power Dissipation (Note 1,3)		$T_A=100^\circ C$		-	0.9	
Junction and Storage Temperature Range	T_J, T_{STG}			-55	150	°C

■ Thermal Resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	$R_{\theta JA}$	-	55	°C/W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	1.9	

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJG150GP15A	F1	YJG150GP15A	5000	10000	100000	13" reel



YJG150GP15A

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A, T_j=25^\circ C$	-150	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-150V, V_{GS}=0V, T_j=25^\circ C$	-	-	-1	μA
		$V_{DS}=-150V, V_{GS}=0V, T_j=150^\circ C$	-	-	-100	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V, T_j=25^\circ C$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A, T_j=25^\circ C$	-1.4	-1.9	-2.4	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-10A, T_j=25^\circ C$	-	110	150	$m\Omega$
		$V_{GS}=-4.5V, I_D=-5A, T_j=25^\circ C$	-	120	180	$m\Omega$
Diode Forward Voltage	V_{SD}	$I_S=-10A, V_{GS}=0V, T_j=25^\circ C$	-	-0.87	-1.2	V
Gate Resistance	R_G	$f=1MHz, T_j=25^\circ C$	-	5	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=-75V, V_{GS}=0V, f=1MHz, T_j=25^\circ C$	-	830	-	pF
Output Capacitance	C_{oss}		-	86	-	
Reverse Transfer Capacitance	C_{rss}		-	6.3	-	
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=-10V, V_{DS}=-75V, I_D=-10A, T_j=25^\circ C$	-	17	-	nC
Gate-Source Charge	Q_{gs}		-	2.4	-	
Gate-Drain Charge	Q_{gd}		-	2.8	-	
Reverse Recovery Charge	Q_{rr}	$I_F=-10A, di/dt=100A/\mu s, V_{GS}=0V, V_R=-75V, T_j =25^\circ C$	-	193	-	nC
Reverse Recovery Time	t_{rr}		-	66	-	ns
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-10V, V_{DS}=-75V, I_D=-10A, R_{GEN}=3\Omega, T_j =25^\circ C$	-	5.4	-	ns
Turn-on Rise Time	t_r		-	4	-	
Turn-off Delay Time	$t_{D(off)}$		-	22	-	
Turn-off Fall Time	t_f		-	7.4	-	

Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of R_{QJA} is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- Thermal resistance from junction to soldering point (on the exposed drain pad).

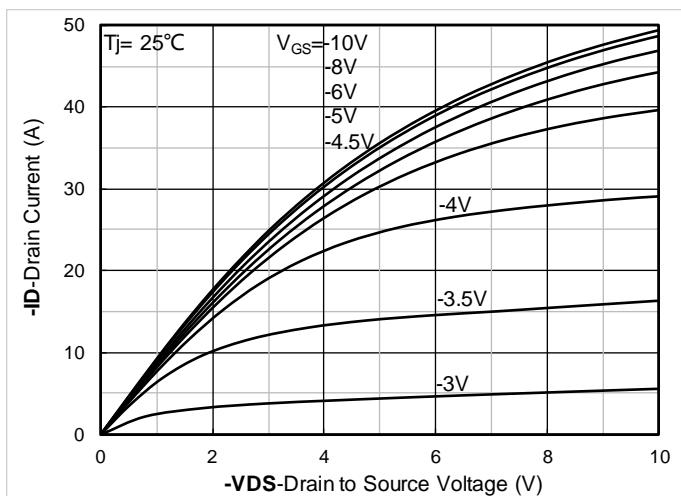
**■ Typical Electrical and Thermal Characteristics Diagrams**

Figure 1. Output Characteristics; typical values

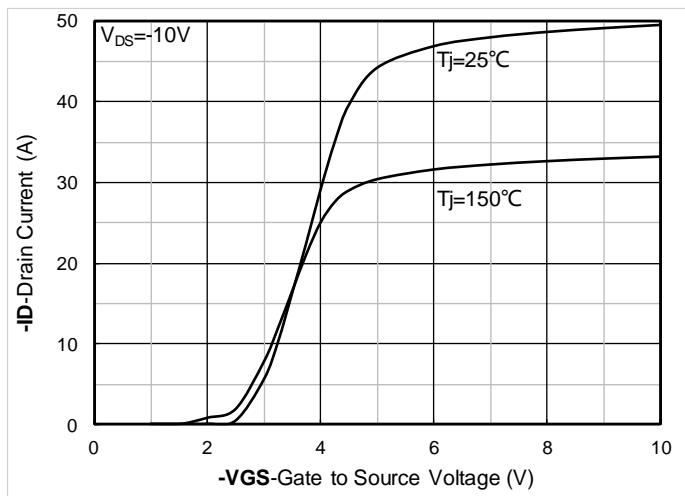


Figure 2. Transfer Characteristics; typical values

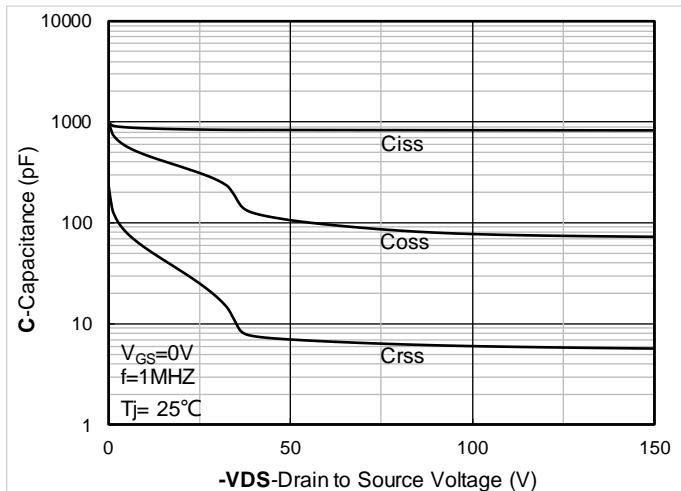


Figure 3. Capacitance Characteristics; typical values

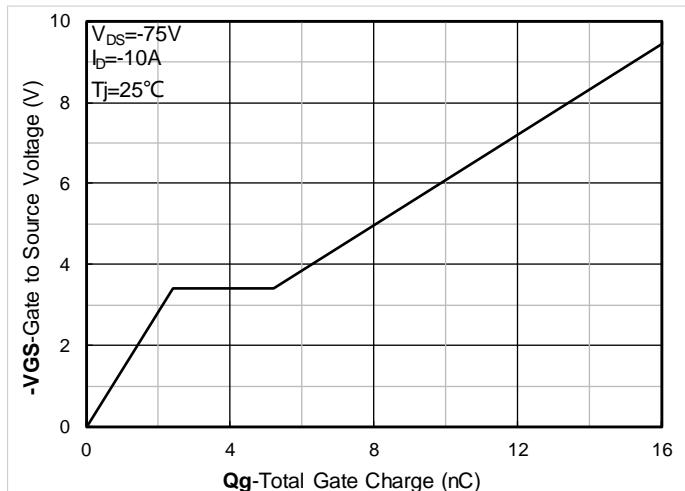


Figure 4. Gate Charge; typical values

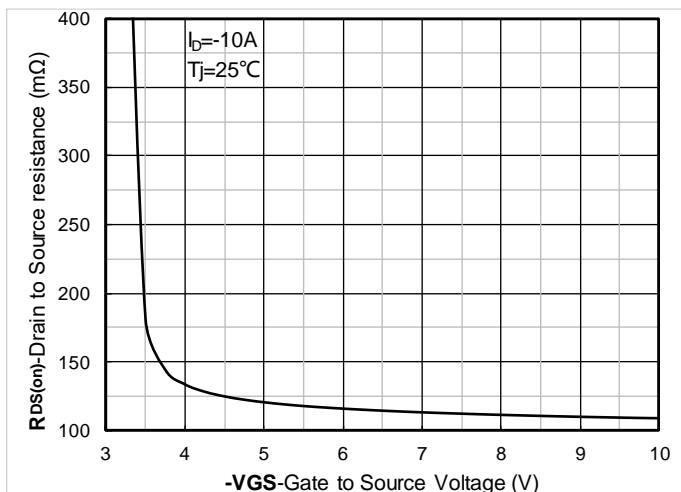


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

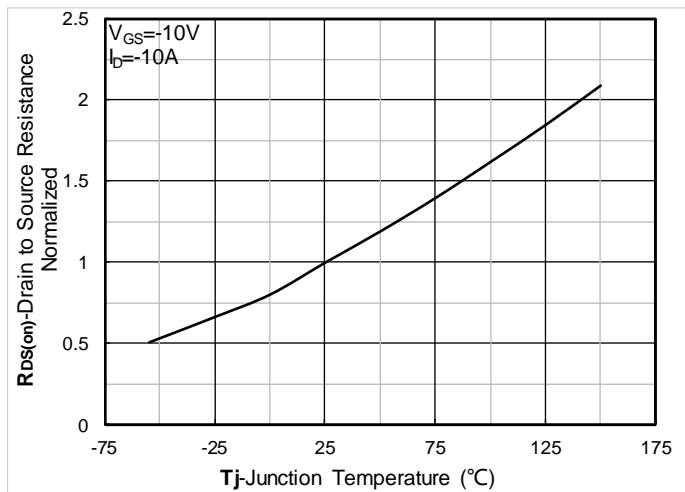


Figure 6. Normalized On-Resistance



YJG150GP15A

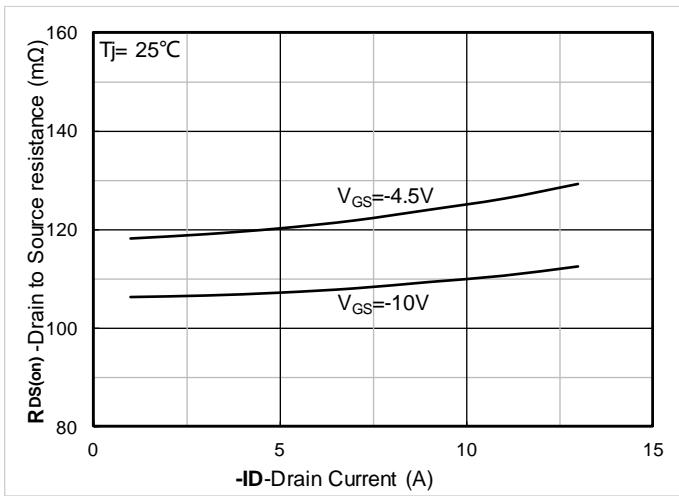


Figure 7. $R_{DS(on)}$ vs. Drain Current; typical values

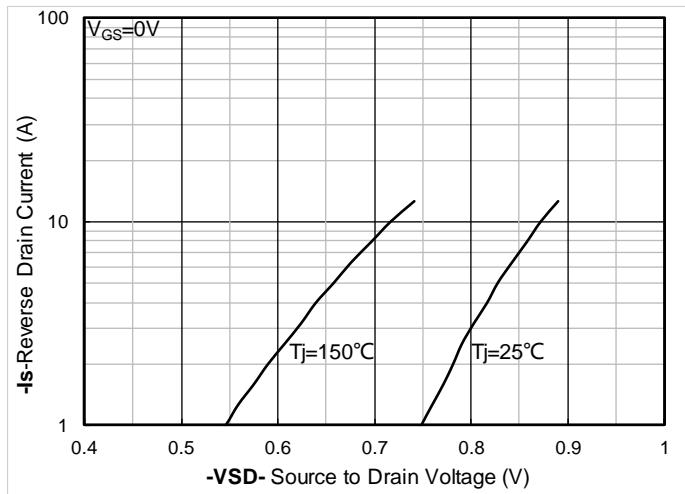


Figure 8. Forward characteristics of reverse diode; typical values

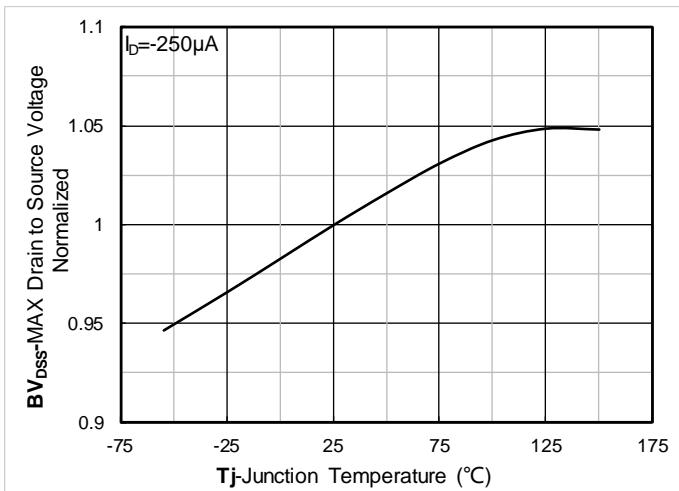


Figure 9. Normalized breakdown voltage

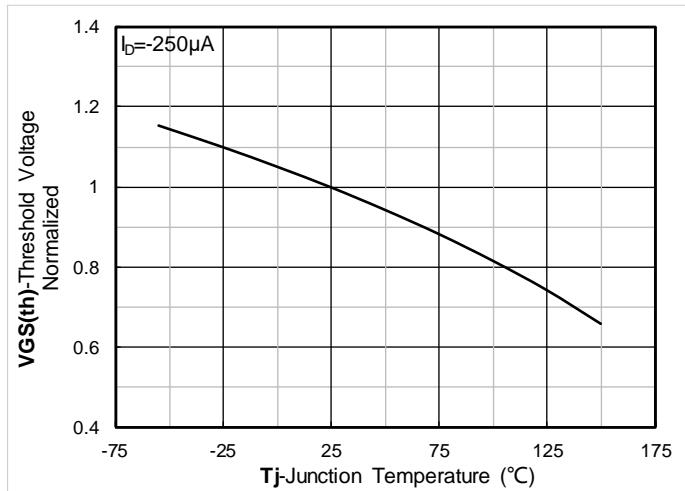


Figure 10. Normalized Threshold voltage

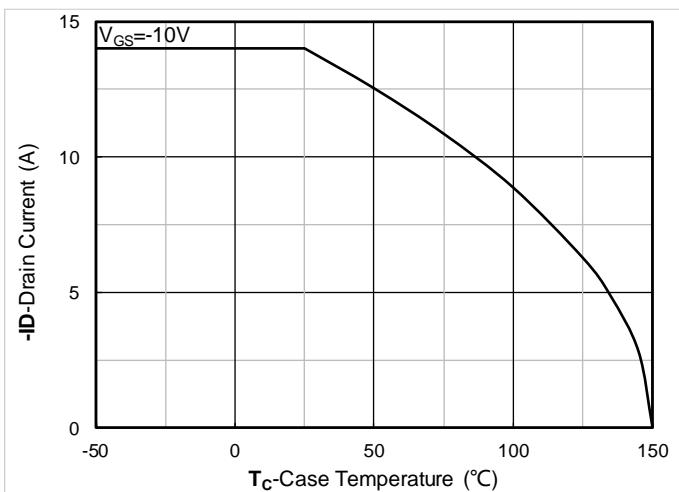


Figure 11. Current dissipation

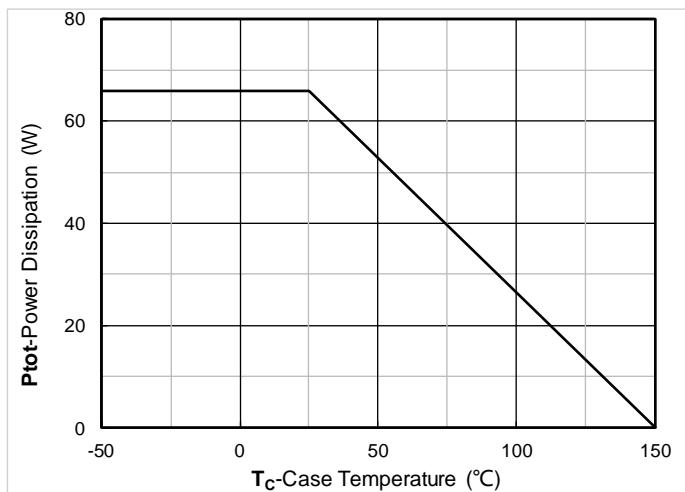


Figure 12. Power dissipation

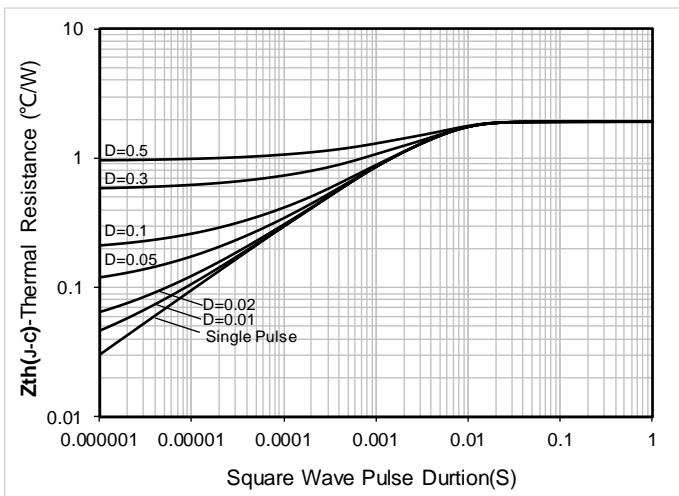


Figure 13. Maximum Transient Thermal Impedance

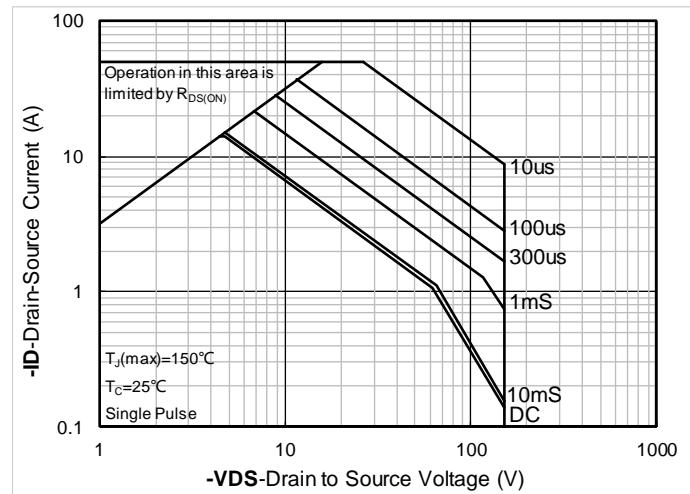
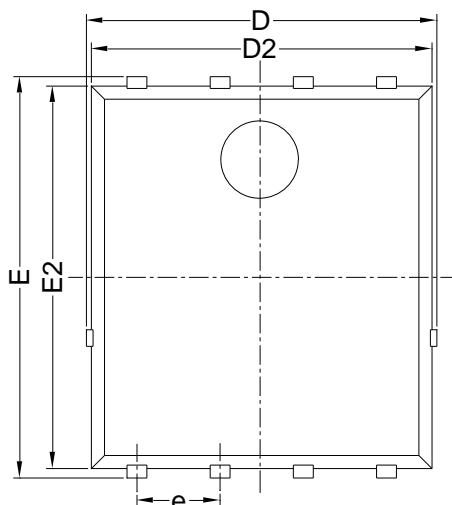
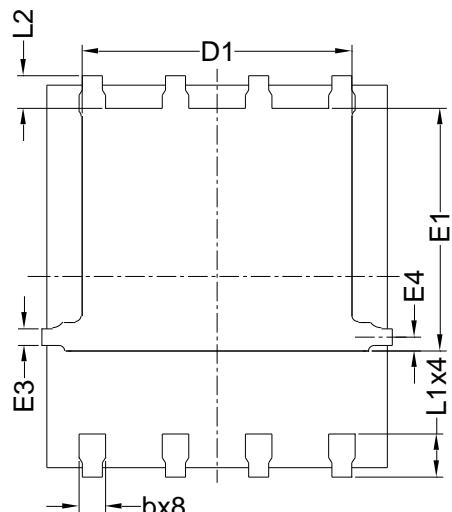
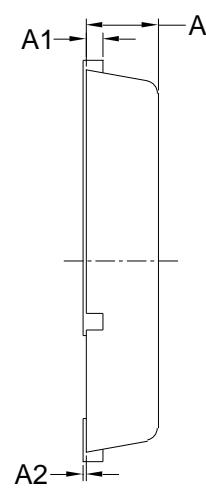
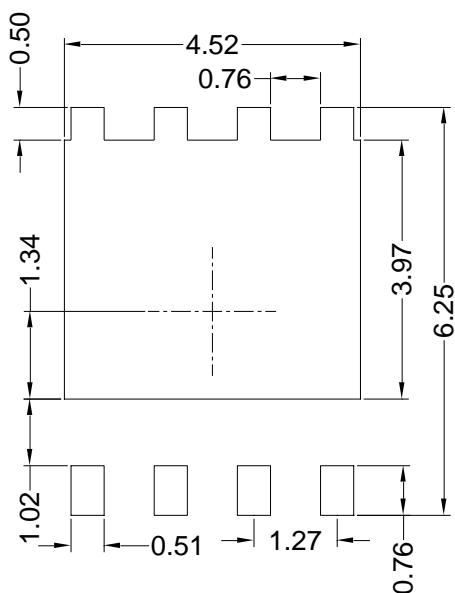


Figure 14. Safe Operation Area



■ PDFN5060-8L-B-1.1MM Package Information

Top View
正面视图Bottom View
背面视图Side View
侧面视图Suggested Solder Pad Layout
Top View

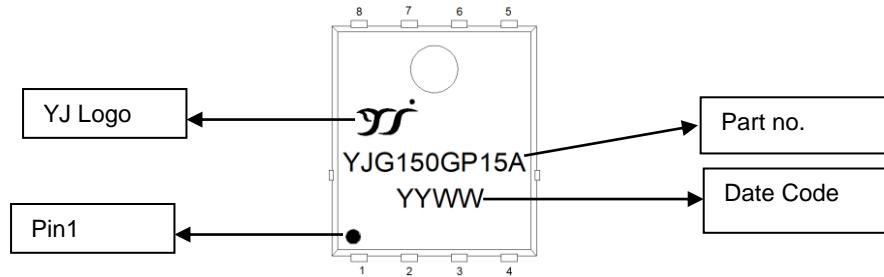
SYMBOL	MILLIMETER		
	MIN	NOM	MAX
D	5.15	5.35	5.55
E	5.95	6.15	6.35
A	1.00	1.10	1.20
A1	0.254 BSC		
A2			0.10
D1	3.92	4.12	4.32
E1	3.52	3.72	3.92
D2	5.00	5.20	5.40
E2	5.66	5.86	6.06
E3	0.254 REF		
E4	0.21 REF		
L1	0.56	0.66	0.76
L2	0.50 BSC		
b	0.31	0.41	0.51
e	1.27 BSC		

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.10\text{mm}$.
3. The pad layout is for reference purposes only.



■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. YJG150GP15A is part no., YYWW is date code, "YY" is year, "WW" is week
4. Body color: Black



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The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use or sale.

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